

## EAST Search History

| Ref # | Hits | Search Query   | DBs                                       | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L4    | 186  | (365/189.04,189.07,189.12,220,221,230.03,233,240).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102" and (multiple or plurality or target or source or first with second) near4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near4 (memory\$4 or buffer\$4 or register\$4) and synchron\$8 with clock\$4   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR               | OFF     | 2006/09/27 08:43 |
| L5    | 41   | (365/189.04,189.07,189.12,220,221,230.03,233,240).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102" and (multiple or plurality or target or source or first with second) near4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near4 (memory\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj clock\$4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR               | OFF     | 2006/09/27 08:44 |
| L6    | 1    | (365/189.04,189.07,189.12,220,221,230.03,233,240).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102" and (multiple or plurality or target or source or first with second) near4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near4 (memory\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj clock\$4 with (enable or writ\$4 or read\$4 or read\$4) adj4 clock\$4 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR               | OFF     | 2006/09/27 08:45 |

## EAST Search History

|    |   |  |   |    |     |                  |
|----|---|--|---|----|-----|------------------|
| L7 | 2 | (365/189.04,189.07,189.12,220,221,230.03,233,240).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102" and (multiple or plurality or target or source or first with second) near4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near4 (memory\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj clock\$4 with (enable or writ\$4 or read\$4 or read\$4) adj4 signal | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR | OFF | 2006/09/27 08:47 |
| L8 | 1 | (327/141,144,145).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102" and (multiple or plurality or target or source or first with second) near4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near4 (memory\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj clock\$4 with (enable or writ\$4 or read\$4 or read\$4) adj4 signal                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR | OFF | 2006/09/27 08:48 |
| L9 | 3 | (327/141,144,145).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102" and (multiple or plurality or target or source or first with second) near4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near4 (memory\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj clock\$4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO | OR | OFF | 2006/09/27 08:48 |